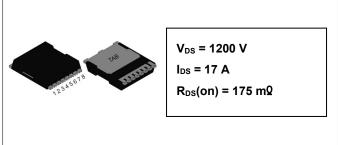
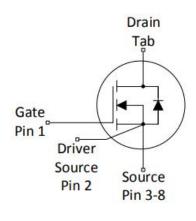


**Circuit Diagram** 



# S2M0160120T 1200V SIC POWER MOSFET





## Description

S2M0160120T is single SiC Power MOSFET packaged in TOLL case. The device is a high voltage n-channel enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0160120T is ideal for energy sensitive, high frequency applications in challenging environments.

#### **Features**

- Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ. RDS(on) =  $175m\Omega$ .
- Fast switching speed and low switching losses.
- · Very fast and robust intrinsic body diode.
- Process of non-bright Tin electroplatin

#### **Applications**

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS
- SMPS (Switch Mode Power Supplies)
- DC-DC Converters
- ESS (Energy Storage Systems)

#### Maximum Ratings(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Max.	Units
Drain Source Voltage	V <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>DS</sub> = 100uA, T <sub>C</sub> = 25°C	1200	V
Gate Source Voltage	V <sub>GSS</sub>	T <sub>C</sub> = 25°C, Absolute maximum values, AC (f>1Hz)	-10 to +25	V
Gate Source Voltage	V <sub>GSOP</sub>	T <sub>C</sub> = 25°C Recommended Operational Values	-5 to +20	V
Continuous Drain Current	I <sub>D</sub>	V <sub>GS</sub> = 20V, T <sub>C</sub> = 25°C	17	Α
	ΙD	V <sub>GS</sub> = 20V, T <sub>C</sub> = 100°C	12	А
Pulsed Drain Current	I <sub>D,pulse</sub>	Pulse width t <sub>P</sub> limited by T <sub>jmax</sub>	40	А
Power Dissipation	PD	T <sub>C</sub> =25°C, T <sub>J</sub> = 175 °C	121	W

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### **Electrical Characteristics(T=25℃ unless otherwise specified)**

Characteristics	Symbol	Condition	Min.	Тур.	Max.	Units	
Drain Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 100 uA	1200			V	
0 / 7   1   1   1   1   1   1   1   1   1	.,	$V_{DS} = V_{GS}, I_D = 2.5 \text{ mA}$	2.0	2.8	4	V	
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 2.5 mA, T <sub>J</sub> = 175 °C		1.9		V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 1200 V, V <sub>GS</sub> = 0 V		1	100	uA	
Gate Source Leakage Current	Igss	V <sub>GS</sub> = 20 V, V <sub>DS</sub> = 0 V			250	nA	
Drain Source On-State	-	V <sub>GS</sub> = 20 V, I <sub>D</sub> = 10 A		175	196	mΩ	
Resistance	$R_{DS(on)}$	V <sub>GS</sub> = 20 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C		300		mΩ	
		V <sub>DS</sub> = 20 V, I <sub>D</sub> = 10 A		3.3		S	
Transconductance	gfs	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 10 A, T <sub>J</sub> = 175 °C		3.4		S	
Input Capacitance	C <sub>ISS</sub>	$V_{GS} = 0 \text{ V},$ $V_{DS} = 1000 \text{ V}$ $V_{AC} = 25 \text{ mV}$ $f = 100 \text{ kHz}$		513			
Output Capacitance	Coss			35.6		pF	
Reverse Transfer Capacitance	C <sub>RSS</sub>			2.59			
Coss Stored Energy	Eoss			20.5		uJ	
Turn-On Switching Energy	E <sub>ON</sub>	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = -5/+20 V		90.3		_	
Turn-Off Switching Energy	E <sub>OFF</sub>	ID =10 A, RG(ext)=2.5 Ω		54.5		uJ	
Turn-On Delay Time	$t_{\text{d(on)}}$			3.5			
Rise Time	tr	V - 900 V V - 5/20 V		11.8			
Turn-Off Delay Time	$t_{\text{d(off)}}$	$V_{DS} = 800 \text{ V}, V_{GS} = -5/20 \text{ V}$ $I_{D} = 10 \text{ A}, R_{G(ext)} = 2.5 \Omega, R_{L} = 80 \Omega$		7.0		ns	
Fall Time	t <sub>f</sub>	_ , _, _, _, _		13.4			
Internal Gate Resistance	R <sub>G(int)</sub>	f = 1 MHz, VAC = 25 mV, D-S short		6.5		Ω	
Gate to Source Charge	Qgs	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = -5/20 V		7.7			
Gate to Drain Charge	$Q_{gd}$	I <sub>D</sub> = 10 A		8.2		nC	
Total Gate Charge	Qg			26.5			

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### **Reverse Diode Characteristics:**

Characteristics	Symbol	Condition	Тур.	Max.	Units
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 5 A	3.3		V
	V <sub>SD</sub>	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 5 A, T <sub>J</sub> = 175 °C	2.9		V
Continuous Diode Forward Current	Is	V <sub>GS</sub> = -5 V, T <sub>C</sub> = 25 °C	20		Α
Reverse Recovery Time	t <sub>rr</sub>	V <sub>GS</sub> = -5 V, I <sub>SD</sub> = 10 A, T <sub>J</sub> = 25 °C	6.6		ns
Reverse Recovery Charge	Qrr	V <sub>R</sub> = 800 V	0.04		uC
Peak Reverse Recovery Current	I <sub>mm</sub>	dif/dt= 2533 A/µs	11		Α

### **Thermal-Mechanical Specifications:**

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	TJ	-	-55 to +175	°C
Storage Temperature	T <sub>stg</sub>	-	-55 to +175	°C
Typical Thermal Resistance Junction to Case	R <sub>0</sub> JC	DC operation	1.15	°C/W

### **Ordering Information:**

Device	Package	Shipping
S2M0160120T	TOLL	1800pcs/reel

### **Marking Diagram**



Where XXXXX is YYWWL

S2M = Device Type 0160

= R<sub>DS</sub>(on) = Reverse Voltage (1200V) 120

= Package SSG = SSG = Year ww = Week = Lot Number

Cautions: Molding resin

Epoxy resin UL:94V-0

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### **Ratings and Characteristics Curves**

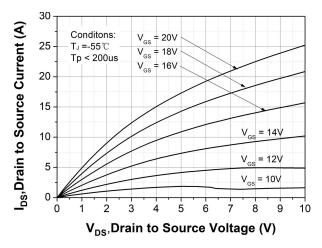


Figure 1. Output Characteristics T<sub>J</sub> = -55 °C

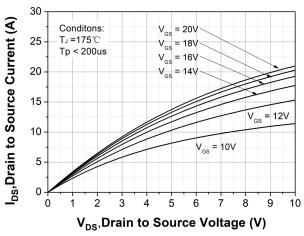


Figure 3. Output Characteristics T<sub>J</sub> = 175°C

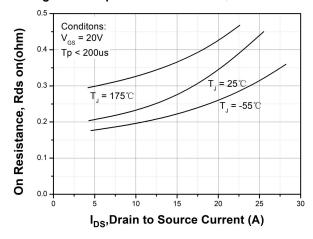


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

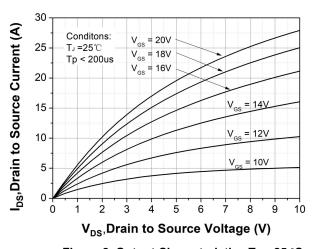


Figure 2. Output Characteristics T<sub>J</sub> = 25 °C

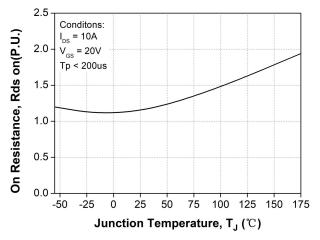


Figure 4. Normalized On-Resistance vs. Temperature

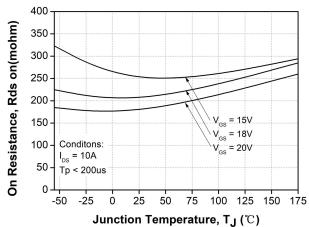
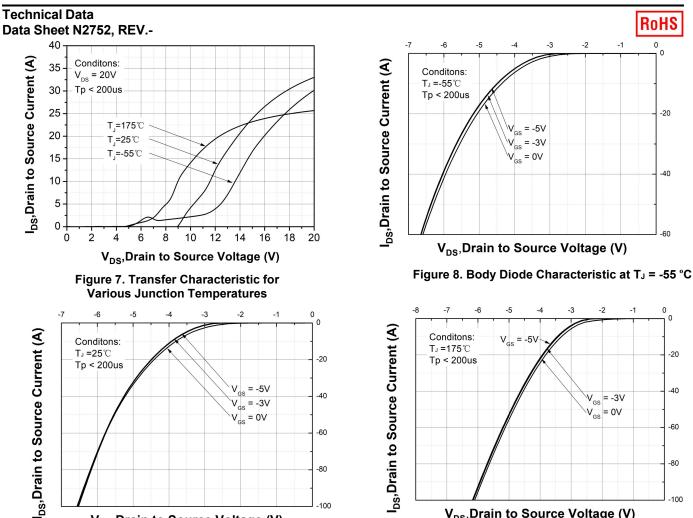


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

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-100

Figure 9. Body Diode Characteristic at T<sub>J</sub> = 25 °C

V<sub>DS</sub>,Drain to Source Voltage (V)

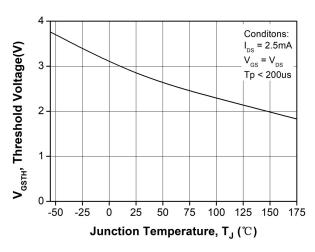


Figure 11. Threshold Voltage vs. Temperature

Figure 10. Body Diode Characteristic at T<sub>J</sub> = 175 °C

V<sub>DS</sub>,Drain to Source Voltage (V)

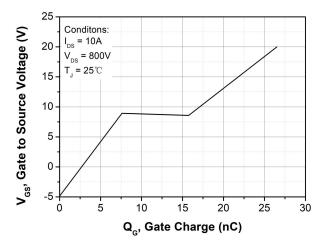


Figure 12. Gate Charge Characteristic

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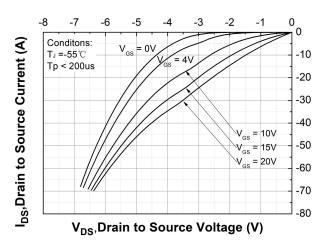


Figure 13. 3rd Quadrant Characteristic at T<sub>J</sub> = -55 °C

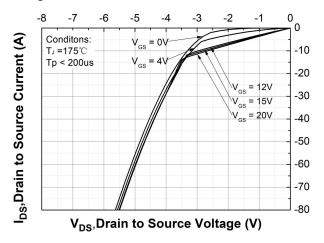


Figure 15. 3rd Quadrant Characteristic at T<sub>J</sub> = 175°C

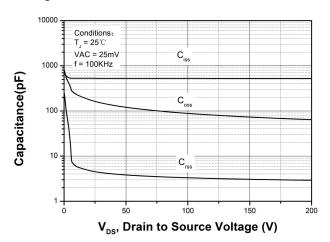


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

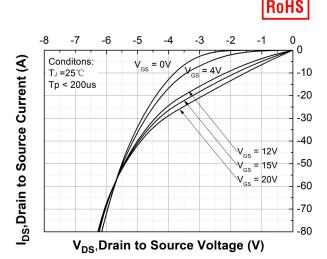


Figure 14. 3rd Quadrant Characteristic at T<sub>J</sub> = 25 °C

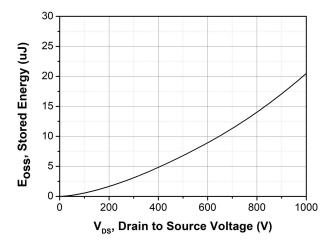


Figure 16. Output Capacitor Stored Energy

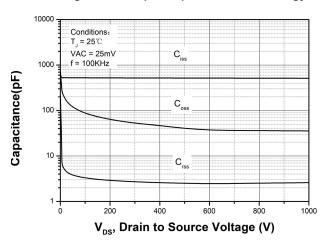


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

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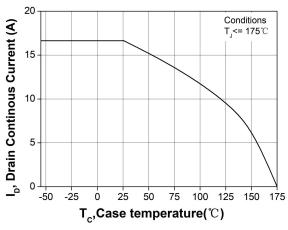


Figure 19. Continuous Drain Current Derating vs.

Case Temperature

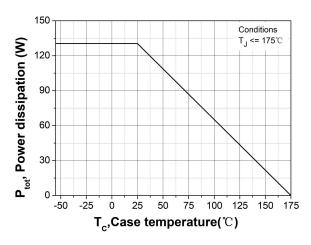


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

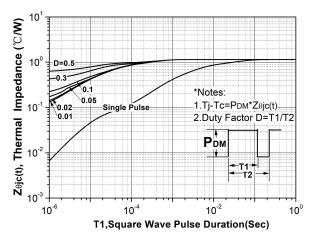


Figure 21. Transient Thermal Impedance (Junction - Case)

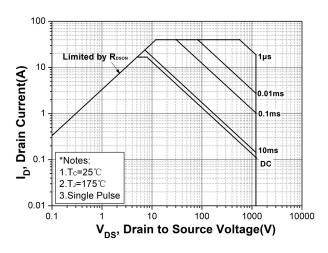


Figure 22. Safe Operating Area

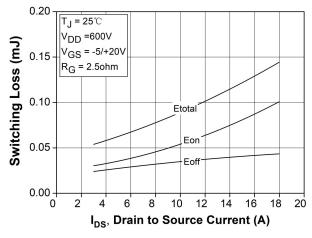


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD}$  = 600V)

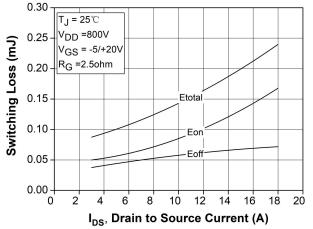


Figure 24. Clamped Inductive Switching Energy vs. Drain Current (V<sub>DD</sub> = 800V)

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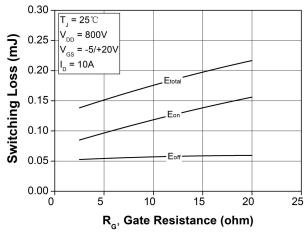


Figure 25. Clamped Inductive Switching Energy vs.  $R_{G(ext)}$ 

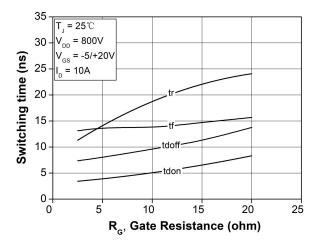


Figure 27. Switching Times vs. R<sub>G(ext)</sub>

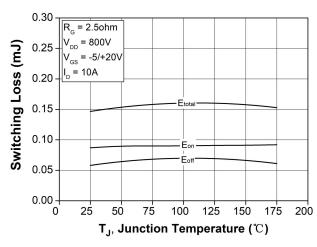


Figure 26. Clamped Inductive Switching Energy vs.
Temperature

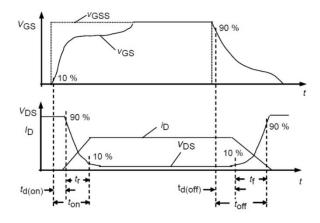
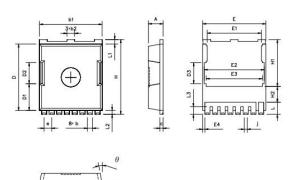


Figure 28. Switching Times Definition





### **Mechanical Dimensions TOLL**



SYMBOL	Dimensions In Millimeters				
31 MBOL	Min.	NOM.	Max.		
A	2.20	2.30	2.40		
b	0.70	0.80	0.90		
b1	9.70	9.80	9.90		
b2		1.20 REF.			
c	0.40	0.50	0.60		
D	10.28	10.38	10.48		
D1	4.08	4.18	4.28		
D2	3.20	3.30	3.40		
D3	3.16	3.26	3.36		
E	9.80	9.90	10.00		
E1	8.40	8.50	8.60		
E2	9.30	9.40	9.50		
E3	8.8 REF.				
E4	0.25	0.35	0.45		
e	1-11-12-12	1.20 BASIC			
Н	11.58	11.68	11.78		
H1	7.23	7.33	7.43		
H2		2.45 REF.			
i	0.10	-	99		
i	0.45REF.				
L	1.60	1.90	2.10		
L1	0.60	0.70	0.80		
L2	0.50	0.60	0.70		
L3	1.05	1.20	1.30		
0	10° REF.				

#### S2M0160120T



#### Technical Data Data Sheet N2752, REV.-



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